FIG.1
Background Art

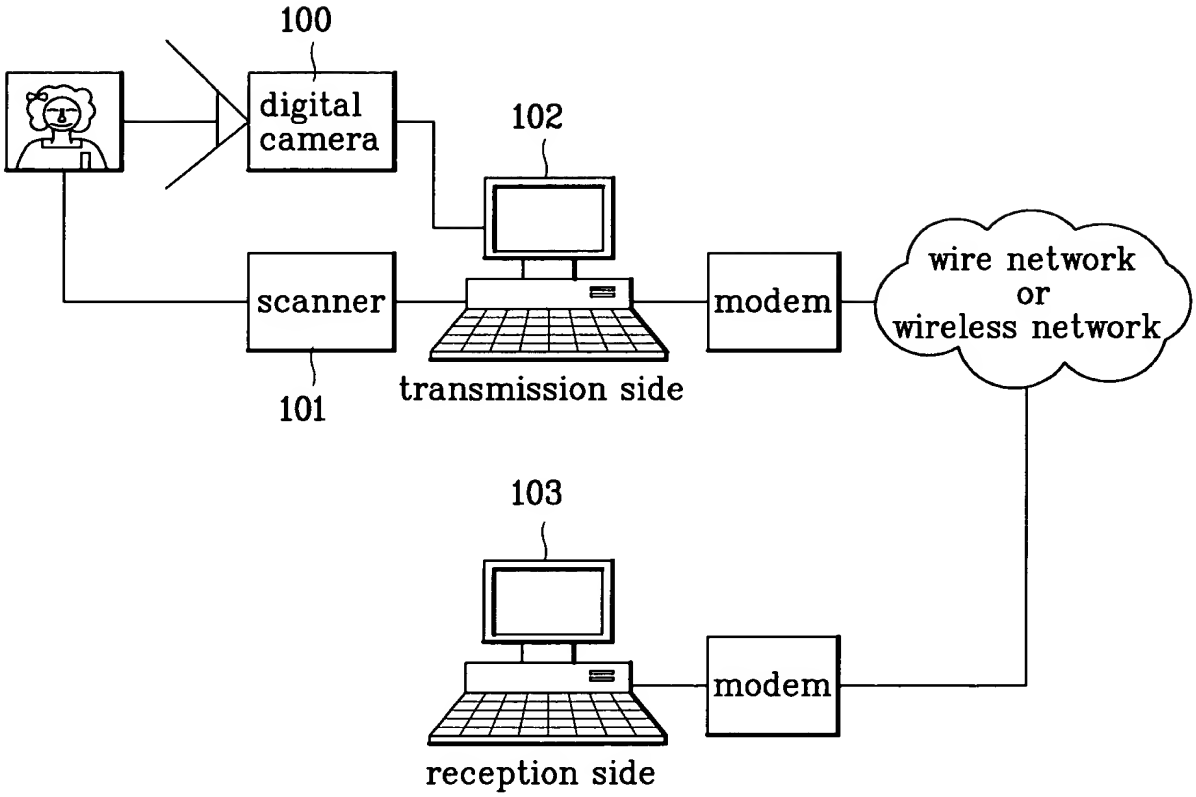


FIG.2
Background Art

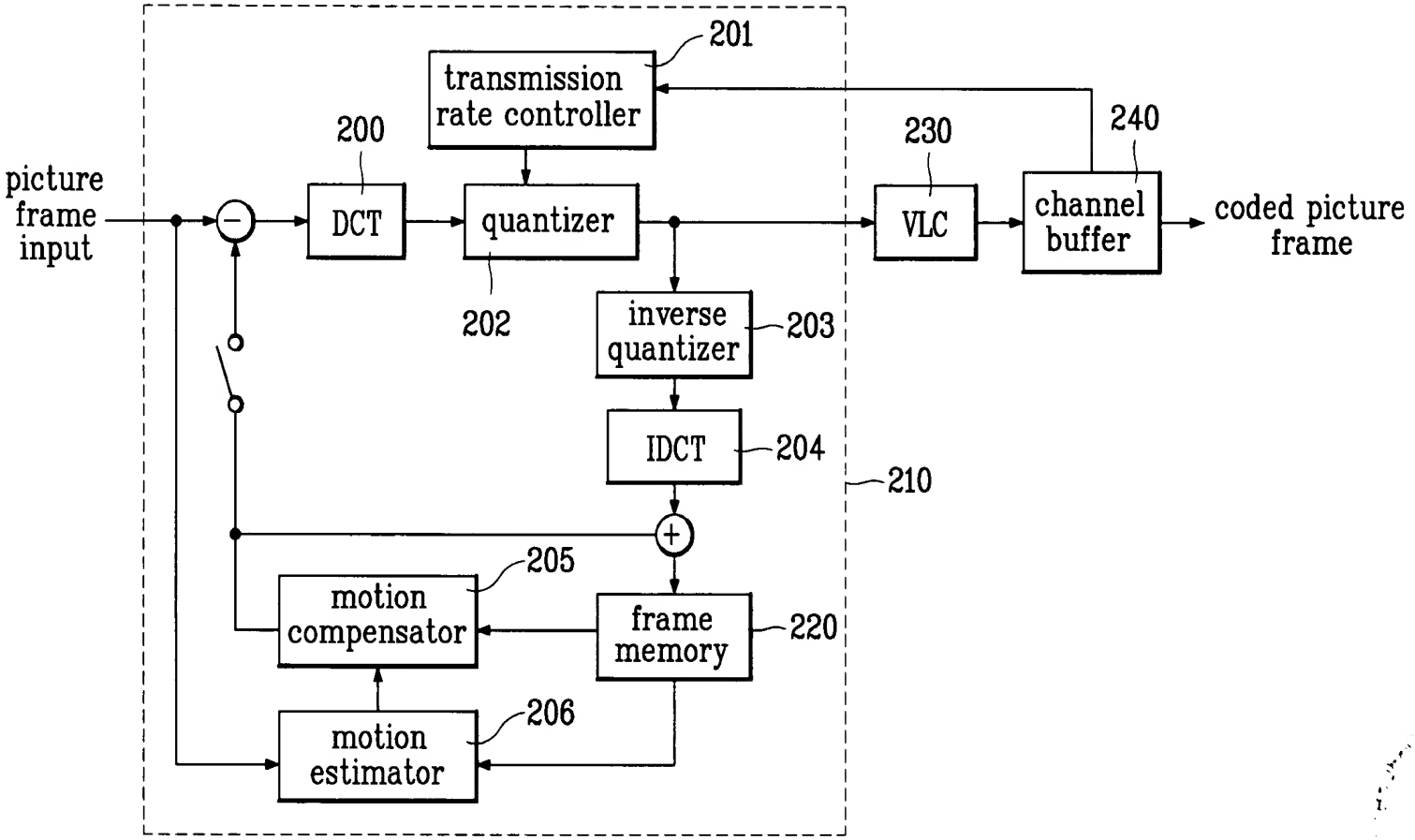


FIG.3
Background Art

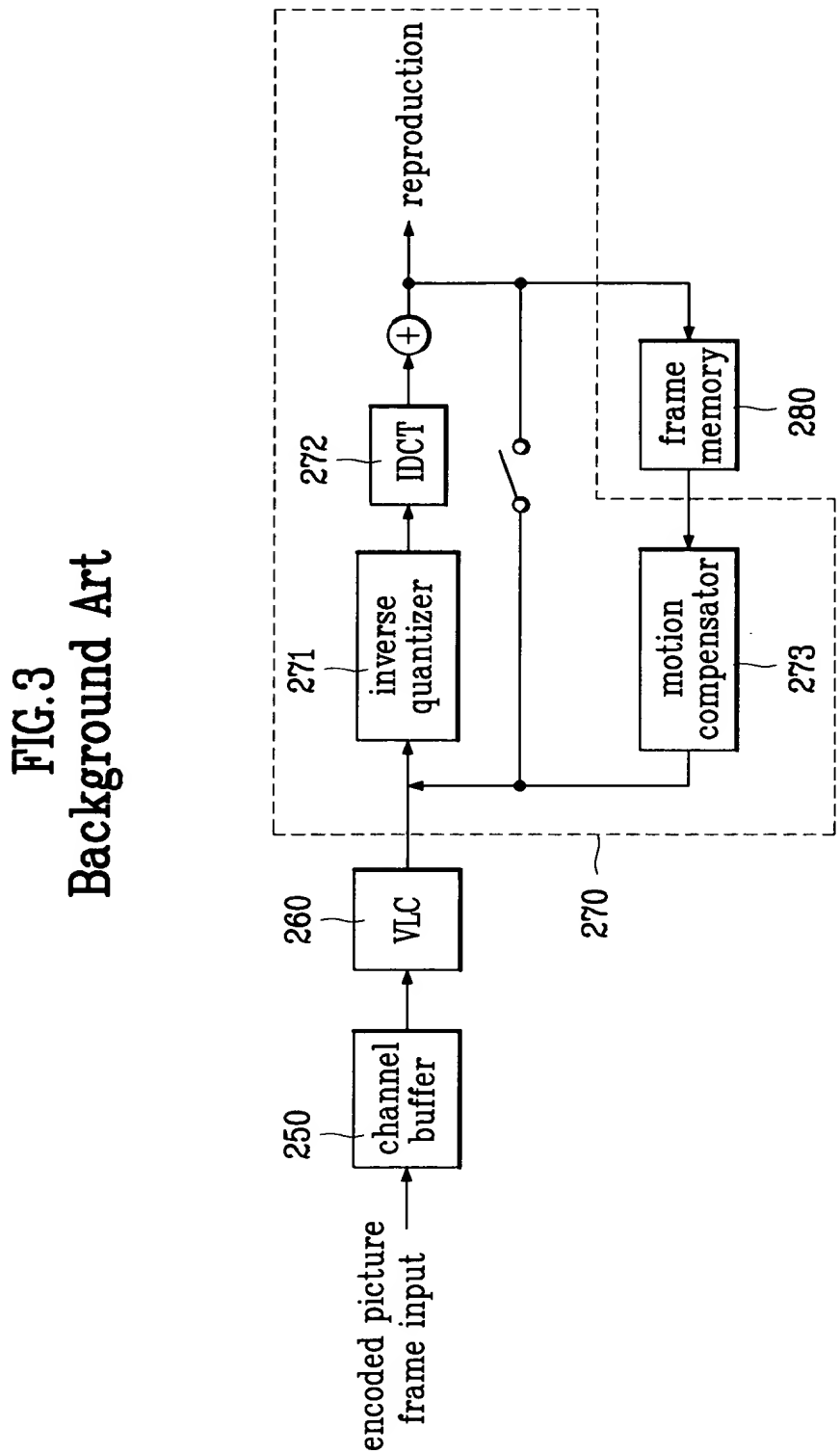


FIG. 4

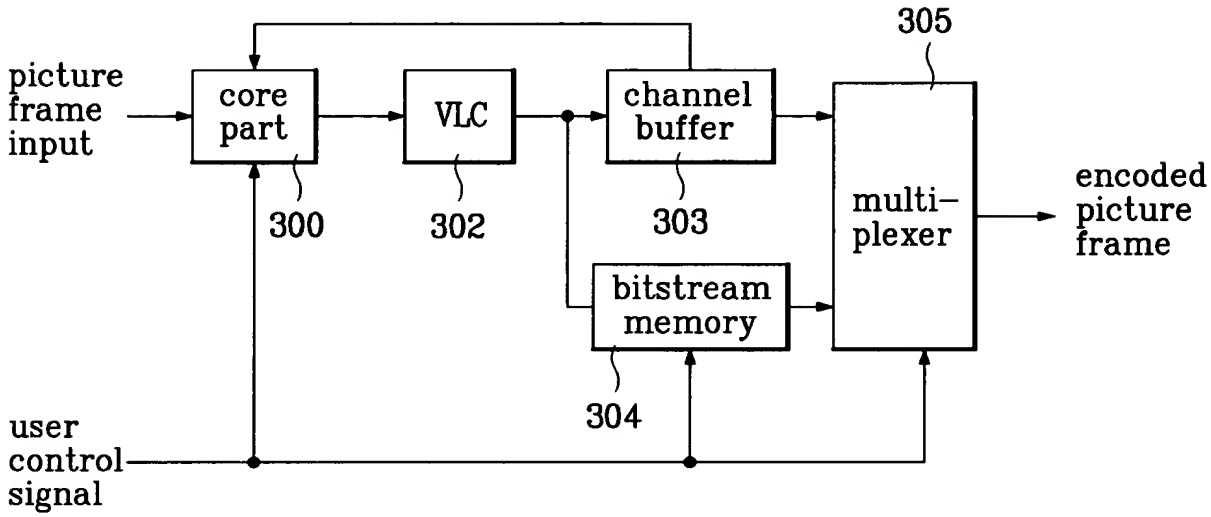

picture frame input
core part
VLC
channel buffer
multi-plexer
encoded picture frame
bitstream memory
300
302
303
304
305
user control signal

FIG. 5

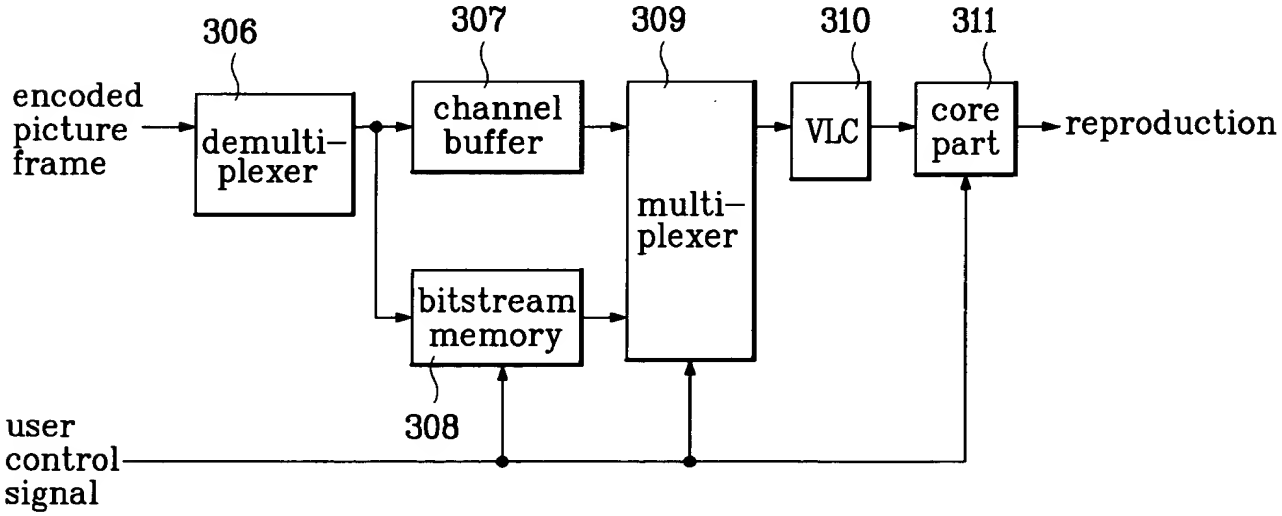

encoded picture frame
demulti-plexer
channel buffer
multi-plexer
VLC
core part
reproduction
bitstream memory
306
307
308
309
310
311
user control signal

FIG. 6

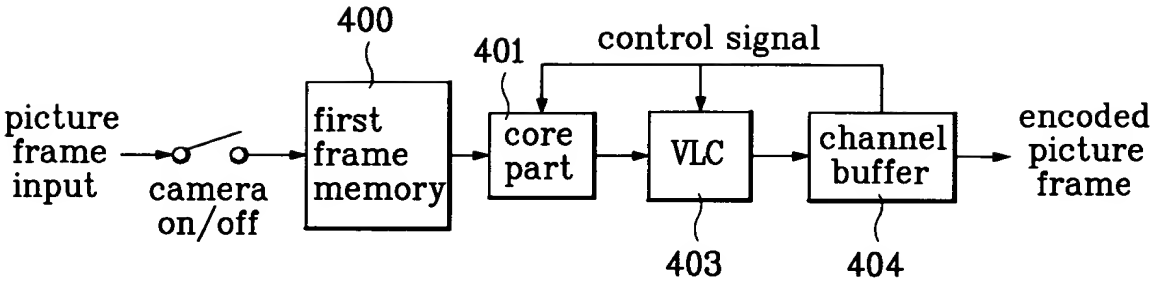


FIG. 7

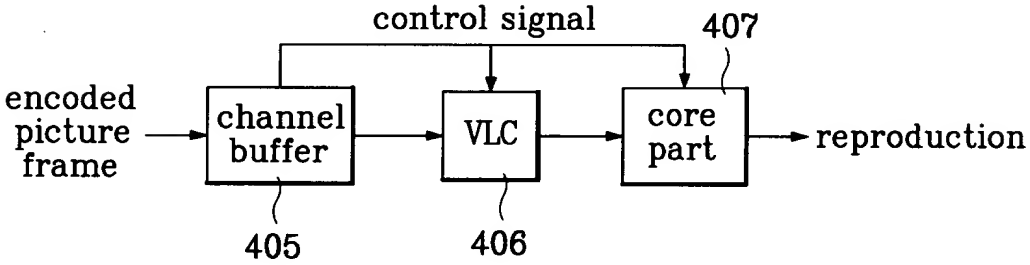


FIG. 8

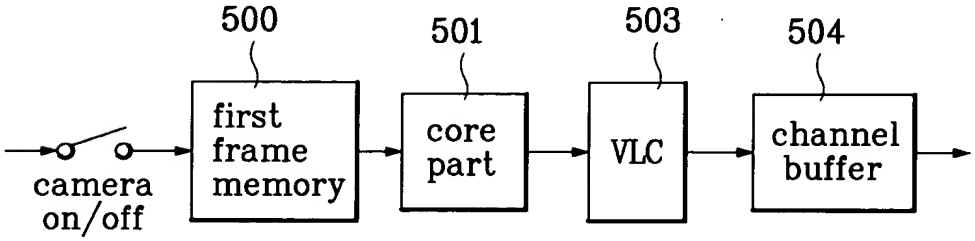


FIG.9

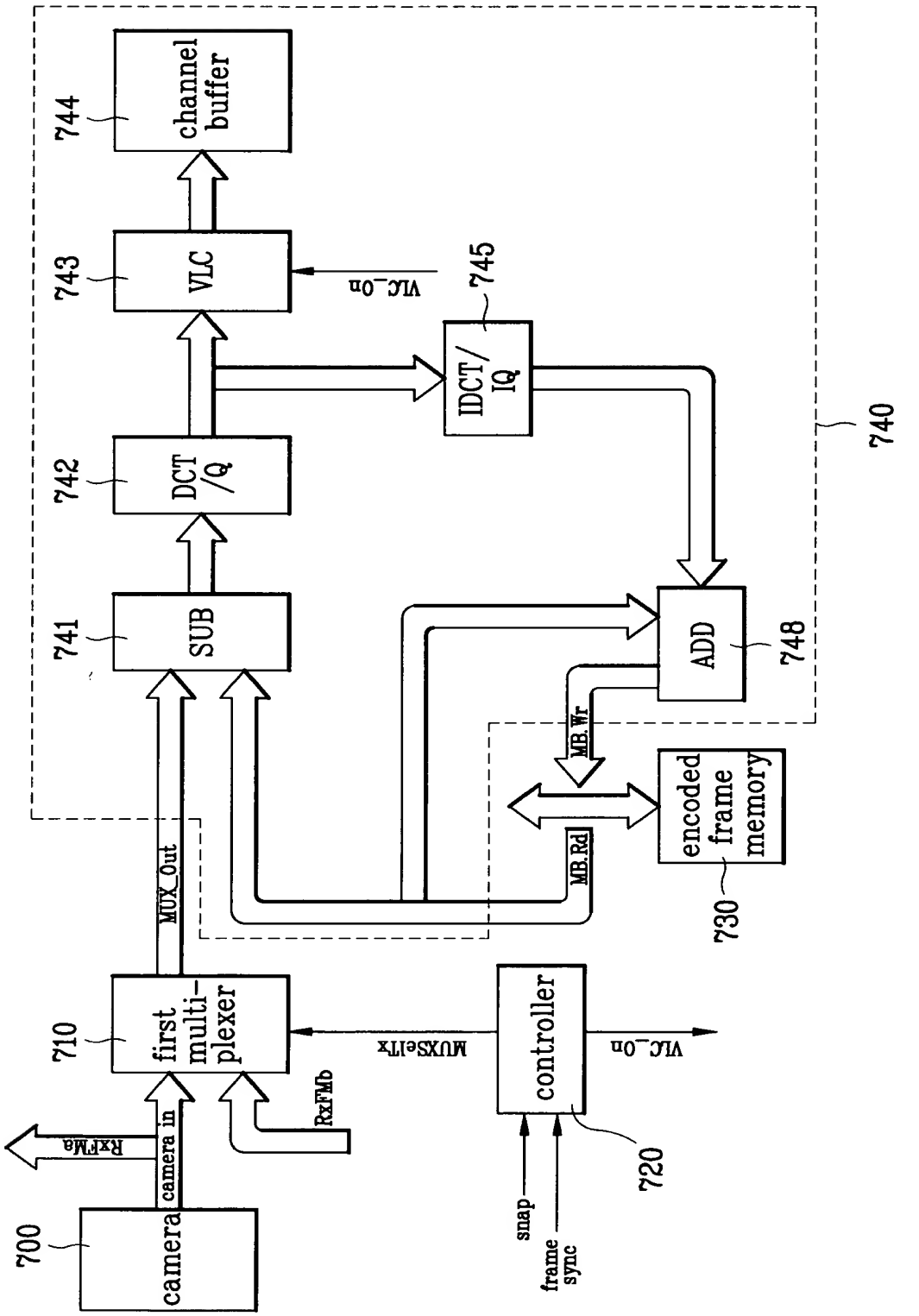

740
744
channel buffer
743
VLC
VLC_On
745
IDCT/IQ
742
DCT/Q
741
SUB
748
ADD
MB.Wr
MB.Rd
encoded frame memory
730
MUX_Out
710
first multi-plexer
MUXSelTx
VLC_On
controller
720
RxFMb
RxFMa
camera in
700
camera
snap
frame sync

FIG.10

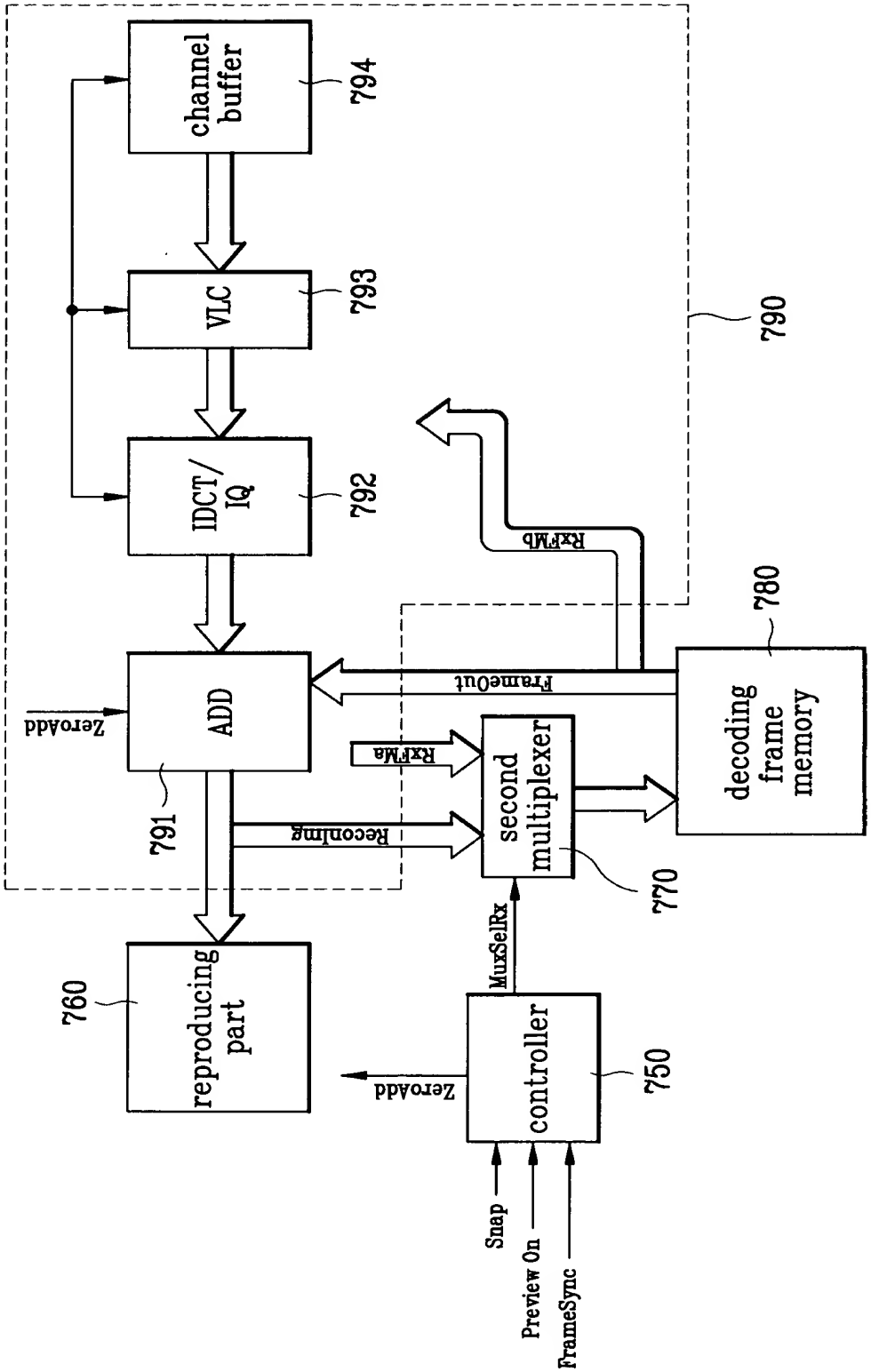

channel buffer
794
VLC
793
IDCT/IQ
792
790
RxFMb
780
ADD
FrameOut
ZeroAdd
RxFMa
second multiplexer
decoding frame memory
791
Reconlmg
770
MuxSelRx
760
reproducing part
controller
750
ZeroAdd
Snap
Preview On
FrameSync

FIG.11

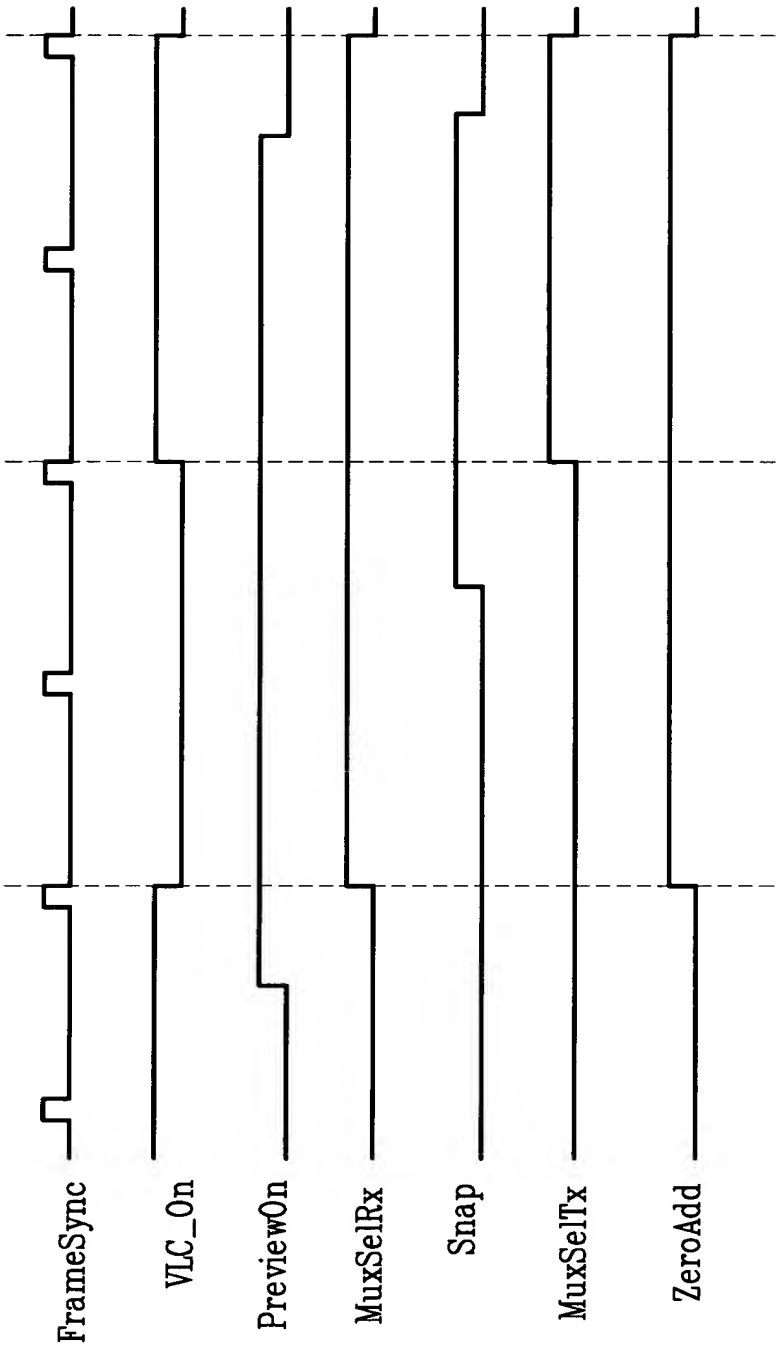

FrameSync
VLC_On
PreviewOn
MuxSelRx
Snap
MuxSelTx
ZeroAdd

FIG.12

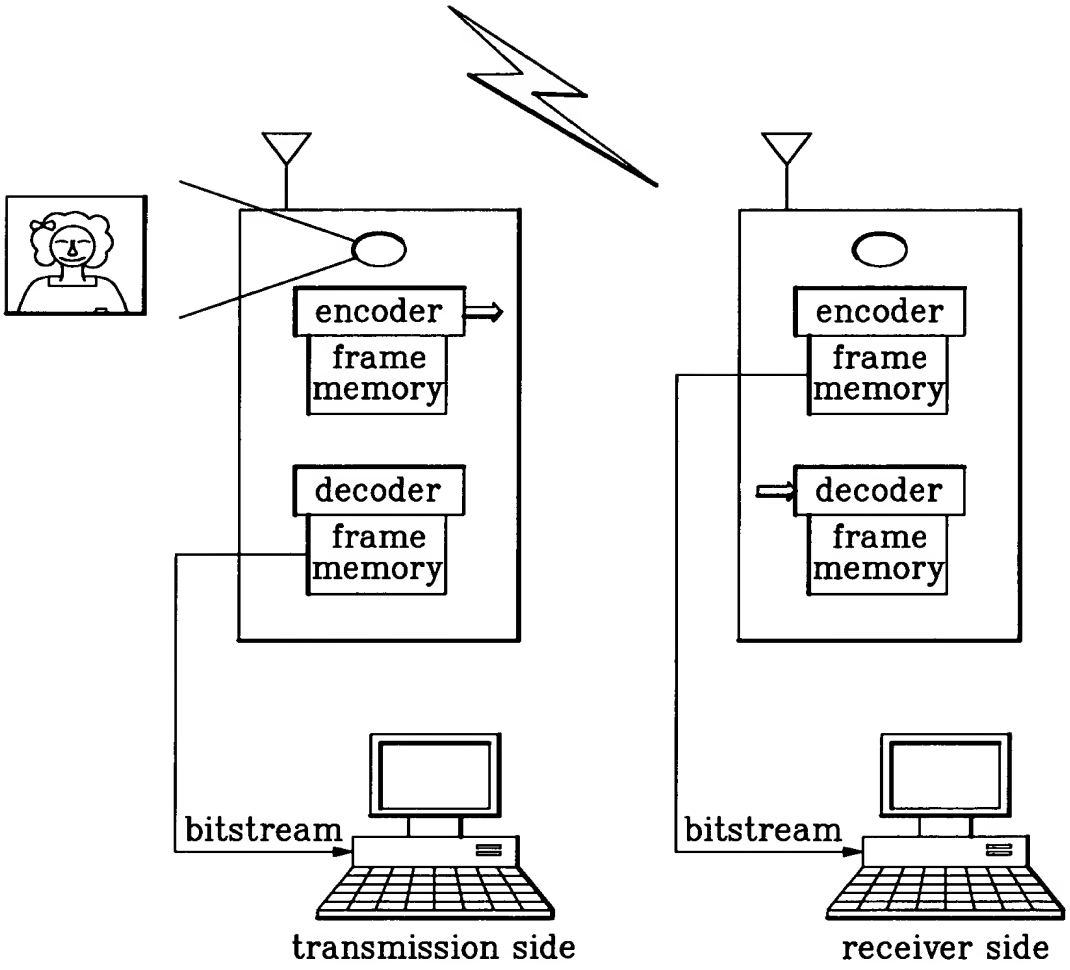
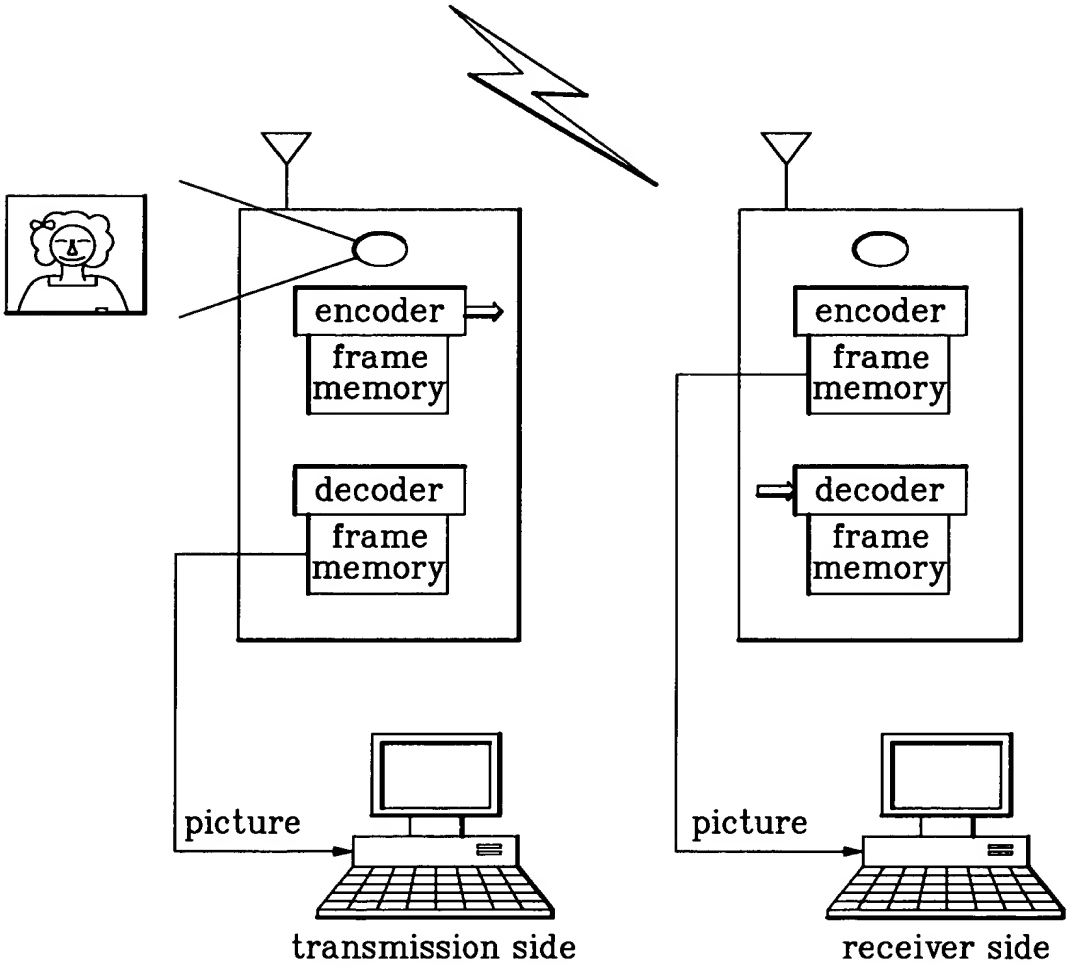

encoder
frame memory
decoder
frame memory
bitstream
transmission side
encoder
frame memory
decoder
frame memory
bitstream
receiver side

FIG.13


encoder
frame memory
decoder
frame memory
picture
transmission side
encoder
frame memory
decoder
frame memory
picture
receiver side